

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	115	"714"/\$.ccls. and (debug\$4 diagnostic) same interrupt same disabl\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 08:33
S3	43	S2 not trac\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 08:33
S7	119	"714"/\$.ccls. and (debug\$4 diagnostic test\$3) adj3 (components device parts) adj3 ("one at a time" separate\$3 individual\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 08:37
S8	65	response with (error fault failure) with (debug\$4 diagnostic test\$3) with (components device parts) with ("one at a time" separate\$3 individual\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 08:39
S9	2	(response adj3 (error fault failure)) with ((debug\$4 diagnostic test\$3) adj3 (components device parts)) with ("one at a time" separate\$3 individual\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 08:41
S11	84	((while when during) adj4 (debug\$4 diagnostic test\$3)) with ((stop\$4 diabl\$3 inhibit\$3 prevent\$3 halt\$3) adj3 (operation function component device)) same switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 08:50
S14	204	((while when during) adj4 (debug\$4 diagnostic test\$3)) same ((stop\$4 diabl\$3 inhibit\$3 prevent\$3 halt\$3) adj3 switch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 08:51
S16	8011	(debug\$4 diagnostic test\$3) adj switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 08:52

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S17	240	S16 same ((stop\$4 diabl\$3 inhibit\$3 prevent\$3 halt\$3) adj3 (operation function component device))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 08:55
S18	117	714/25.ccls. and (debug\$4 diagnostic test\$3) with (components device parts) with ("one at a time" separate\$3 individual\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 09:04
S29	1	714/25.ccls. and ((diagnostic debug\$4) adj3 processor) and (disabl\$4 adj (operations functions))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 09:43
S30	17	714/25.ccls. and ((diagnostic debug\$4) adj3 processor) and (checkpoint\$3 breakpoint\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 09:46
S31	2	puente.xa. and replay\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 09:47
S32	37	(reproduc\$3 replay\$3) with (source adj (fault failure error))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 10:04
S35	100	"714"/\$.ccls. and (error faulr failure) same (single adj step)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 10:22
S36	5	"714"/\$.ccls. and (single adj step) and (reproduc\$3 adj (error fault failure))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 10:54

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S37	15	(single adj step) and (reproduc\$3 adj (error fault failure))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 11:09
S39	632	(error fault failure) same(playback adj mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 10:57
S40	36	"714"/\$.ccls. and (error fault failure) same (playback adj mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 10:58
S41	20	(diagnostic debug\$4) same (playback adj mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 10:59
S42	415	(diagnostic debug\$4) same (playback)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 11:01
S50	60	((single adj step) reproduc\$3 playback) same ((source cause) adj (error fault failure)) same (interrupt stop halt)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 13:07
S51	13	("4814971" "5056091" "5138708" "5515376" "5633857" "5778251" "5911059").PN. OR ("6223305").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/07/13 14:48
S59	7	("5355468" "5724348" "6223305" "6298394" "6349371" "6389557" "6550022").PN. OR ("6772371").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/07/13 15:29
S60	272	714/34.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/07/14 09:38
S61	1	"10/758586" and delay	US-PGPUB; USPAT; USOCR	OR	OFF	2006/07/13 16:10

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S62	2600	(error fault failure) and ((disable halt stop delay) adj (processor bus memory))	US-PGPUB; USPAT; USOCR	OR	OFF	2006/07/13 16:14
S63	317	"714"/\$.ccls. and (error fault failure) and ((disable halt stop delay) adj (processor bus memory))	US-PGPUB; USPAT; USOCR	OR	OFF	2006/07/13 16:14
S67	82	(diagnostic debug\$4) and (switch with ((disable halt stop delay) adj (processor bus memory)))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/13 16:35
S68	2	(diagnostic debug\$4) same (switch with ((disable halt stop delay) adj (processor bus memory)))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/13 16:53
S69	972	(switch with ((disable halt stop delay) adj3 (processor bus memory)))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/13 16:53
S70	443	(switch with ((disabl\$3 halt\$3) adj3 (processor bus memory)))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/13 16:53
S71	62	(error fault failure) same (switch with ((disabl\$3 halt\$3) adj3 (processor bus memory)))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/13 16:56
S72	36	(error fault failure) same switch same register same ((disabl\$3 halt\$3) adj3 (processor bus memory))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/13 17:10
S73	109	"714"/\$.ccls. and (diagnostice debug\$4) same ((disabl\$3 halt\$3) adj3 (processor bus memory))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/13 17:16
S74	180	"714"/\$.ccls. and (diagnostice debug\$4) same ((disabl\$3 halt\$3 freez\$3 stop\$4) adj3 (processor bus memory))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/13 17:21
S76	462	(switch adj5 ((disabl\$3 halt\$3 freez\$3 stop\$4) adj5 (processor bus memory)))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 08:20
S77	40	"714"/\$.ccls. and (switch adj5 ((disabl\$3 halt\$3 freez\$3 stop\$4) adj5 (processor bus memory)))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 08:23
S85	525	(stop inhibit disable) with ((branch adj prediction adj unit) (issue adj unit) dispatch)	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 09:21
S88	135	((stop inhibit disable) adj3 (CPU)) with switch	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 09:22
S89	0	((stop inhibit disable) adj3 (CPU)) same (plurality adj3 switch)	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 09:22

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S90	133	((stop inhibit disable) adj5 (components functions unit) adj3 (CPU))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/14 09:23
S91	629	712/227.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/07/14 09:42
S92	311	S91 and (debug\$4 diagnostic)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/07/14 09:42
S93	34	S91 and (stop inhibit disable) adj5 (components functions unit)	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 09:46
S94	231	S91 and switch	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 09:46
S95	134	S91 and (stop inhibit disable) and switch	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 10:23
S96	20895	(functional adj unit)	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 10:24
S98	334	"714"/\$.ccls. and (functional adj unit) same (debug\$4 test\$3 diagnostic)	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 10:26
S99	18	"714"/\$.ccls. and (functional adj unit) same (debug\$4 test\$3 diagnostic) same (stop inhibit disable)	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 11:36
S10 0	49	"714"/\$.ccls. and (functional adj unit) same switch	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 10:27
S10 1	0	(functional adj unit) same (debug\$4 test\$3 diagnostic) same ((stop inhibit disable) adj3 (memory processor))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 11:37
S10 2	752	(debug\$4 test\$3 diagnostic) same ((stop inhibit disable) adj3 (memory processor))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 11:38
S10 4	408	"714"/\$.ccls. and (debug\$4 test\$3 diagnostic) same ((stop inhibit disable delay) adj3 (memory processor))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 11:52

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S105	22	"714"/\$.ccls. and (debug\$4 test\$3 diagnostic) same ((stop inhibit disable delay) adj3 (memory bus)) and ((stop inhibit disable delay) adj3 processor)	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 11:56
S106	15	"714"/\$.ccls. and (debug\$4 test\$3 diagnostic) same ((stop inhibit disable) adj3 (memory bus)) and ((stop inhibit disable delay) adj3 processor)	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 11:58
S107	14	"714"/\$.ccls. and (debug\$4 test\$3 diagnostic) same ((stop inhibit disable) adj3 (memory bus processor) adj3 (error fault failure))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 12:00
S108	106	((stop inhibit disable) adj3 (memory bus processor) adj3 (error fault failure))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 12:00
S109	3	((stop inhibit disable) adj3 (functional adj units) adj3 (error fault failure))	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/14 12:00
S110	1786	714/25.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/16 10:38
S111	663	714/25.ccls. and (debug\$4 diagnostic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/16 10:39
S112	8103	(debug\$4 diagnostic) and ((halt\$4 disabl\$3 stop\$4 inhibit\$3) adj3 (processor memory bus component device))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/16 10:40
S113	366	"714"/\$.ccls. and (debug\$4 diagnostic) and (delay\$3 adj3 (processor memory bus component device))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/16 10:41
S117	31	"714"/\$.ccls. and (select\$3 adj3 ((debug\$4 diagnostic) adj test))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/16 10:45

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S11 8	1	"6487677".pn. and diagnost\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/16 11:20
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